

Notice of References Cited	Application/Control No. 09/921,789		Applicant(s)/Patent Under Reexamination HATTORI, TAKAHIRO	
	Examiner Zeev Kitov		Art Unit 2836	Page 1 of 1

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
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	B	US-6,369,556	04-2002	Ohshima, Shunzou	323/282
	C	US-6,222,355	04-2001	Ohshima et al.	323/282
	D	US-6,594,129	07-2003	Baba et al.	361/87
	E	US-5,530,302	06-1996	Hamre et al.	307/147
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	M	US-			

FOREIGN PATENT DOCUMENTS

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
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	V	A. Sedra et al., Microelectronic Circuits, 1987, Holt,Rinehart and Winston, pp. 342 - 344.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.